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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Raymond W. Zeng et al.

Art Unit : 2838

Serial No. : 10/056,657

Examiner : Unknown

Filed : October 19, 2001

Title : PROVIDING MULTIPLE VOLTAGE SIGNALS

Commissioner for Patents
Washington, D.C. 20231

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8-22-02
Payton

PRELIMINARY AMENDMENT AND REQUEST FOR APPROVAL OF DRAWINGS
CHANGES

Prior to examination, please amend the application as follows:

In the drawings:

Please amend FIG. 1 as shown in the enclosed drawings.

In the claims:

Add claims 21 - 25.

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a! --21. A method of providing a signal to an Electrically Erasable Programmable Read-Only Memory (EEPROM), comprising:

using a plurality of clock drivers that are selectable to provide an output signal, wherein the selection is in accordance with a mode of operation to provide the output signal at a read level voltage, a program level voltage and a verify level voltage.--

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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April 26, 2002
Zita M. Liston
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